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## ABSTRACT OF THE DISCLOSURE

A higher-radix type divider is provided which is capable of obtaining a quotient at a high speed by performing a scaling on a divisor and by representing a partial remainder in a redundant binary notation.

The divider for obtaining the quotient by referring to the divisor and dividend normalized respectively so as to satisfy a range of  $1/2^{\kappa}$  or more and less than  $1/2^{\kappa+1}$  (k being a positive integer) and to a length of bits, out of all bits of the partial remainder, defined by a radix for operations and a maximum number of digits, is provided with a scaling factor generating section, a multiplying section, a divisor tripled-number generating section and a repetitive operating section.

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